

**Fig. 2**

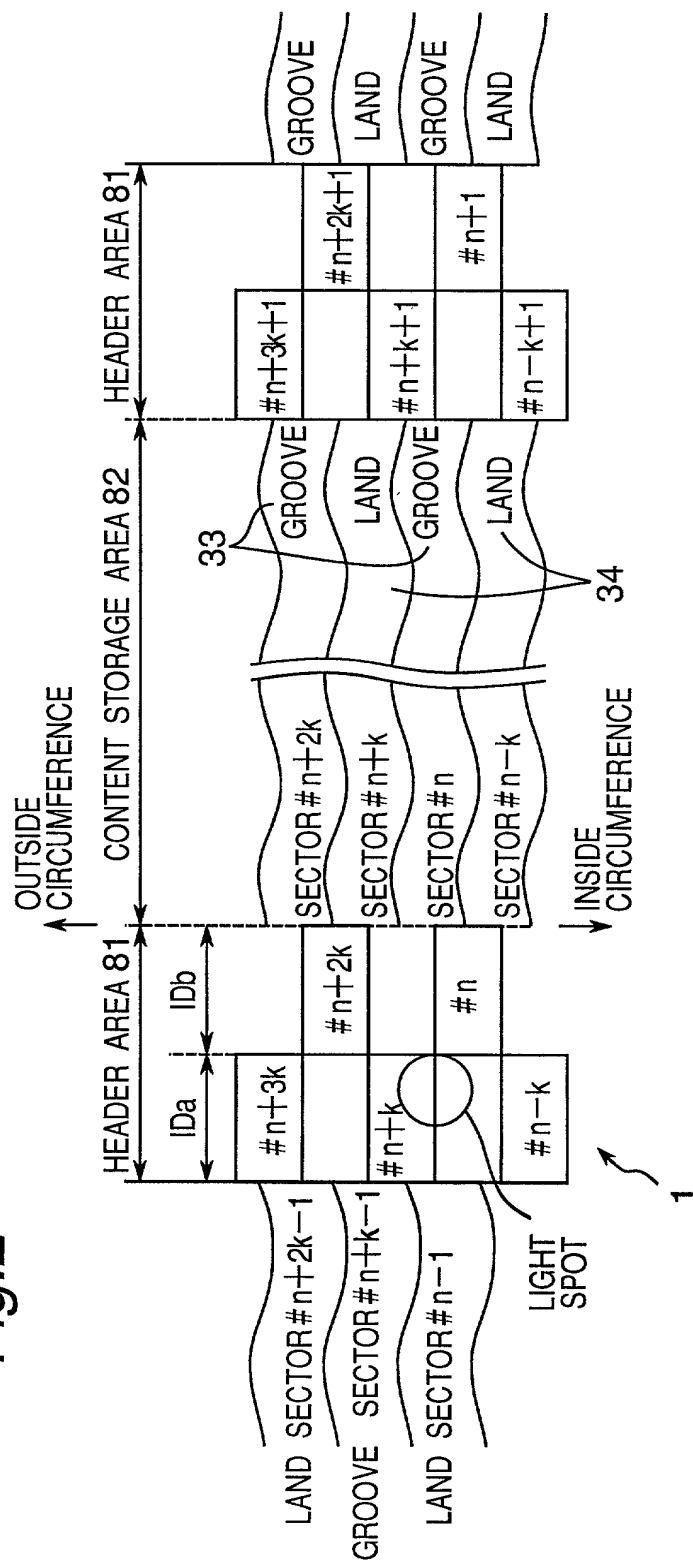
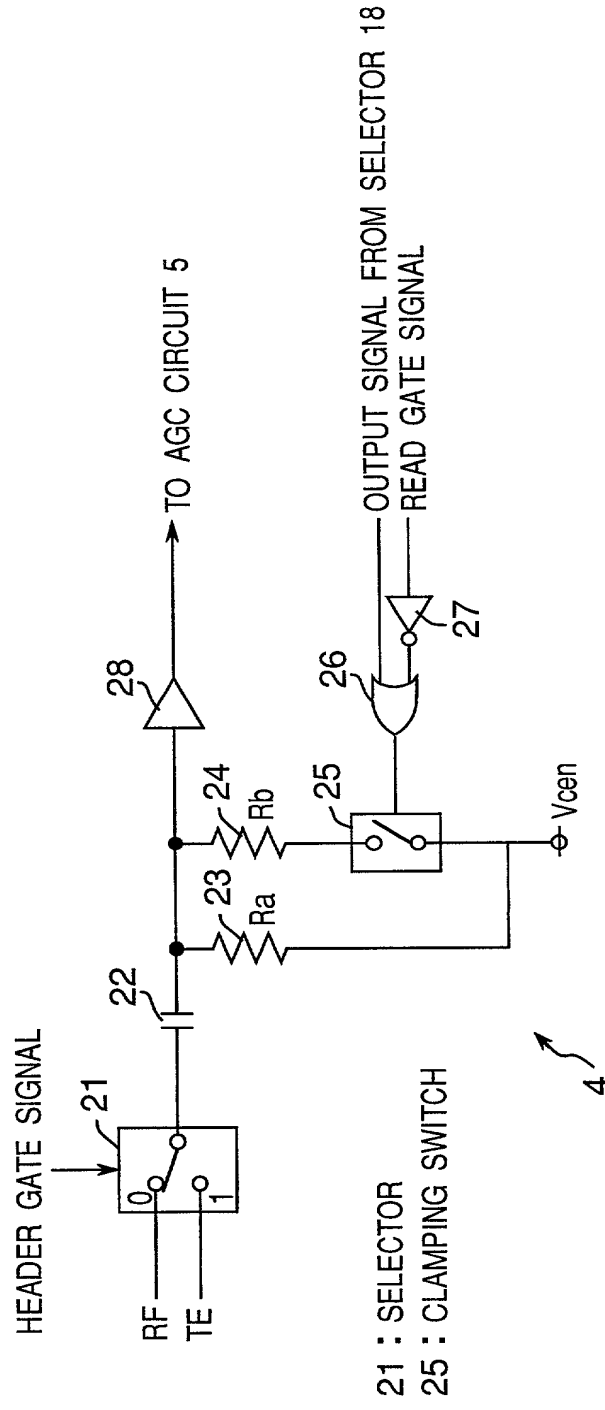




Fig.4



**Fig.5**

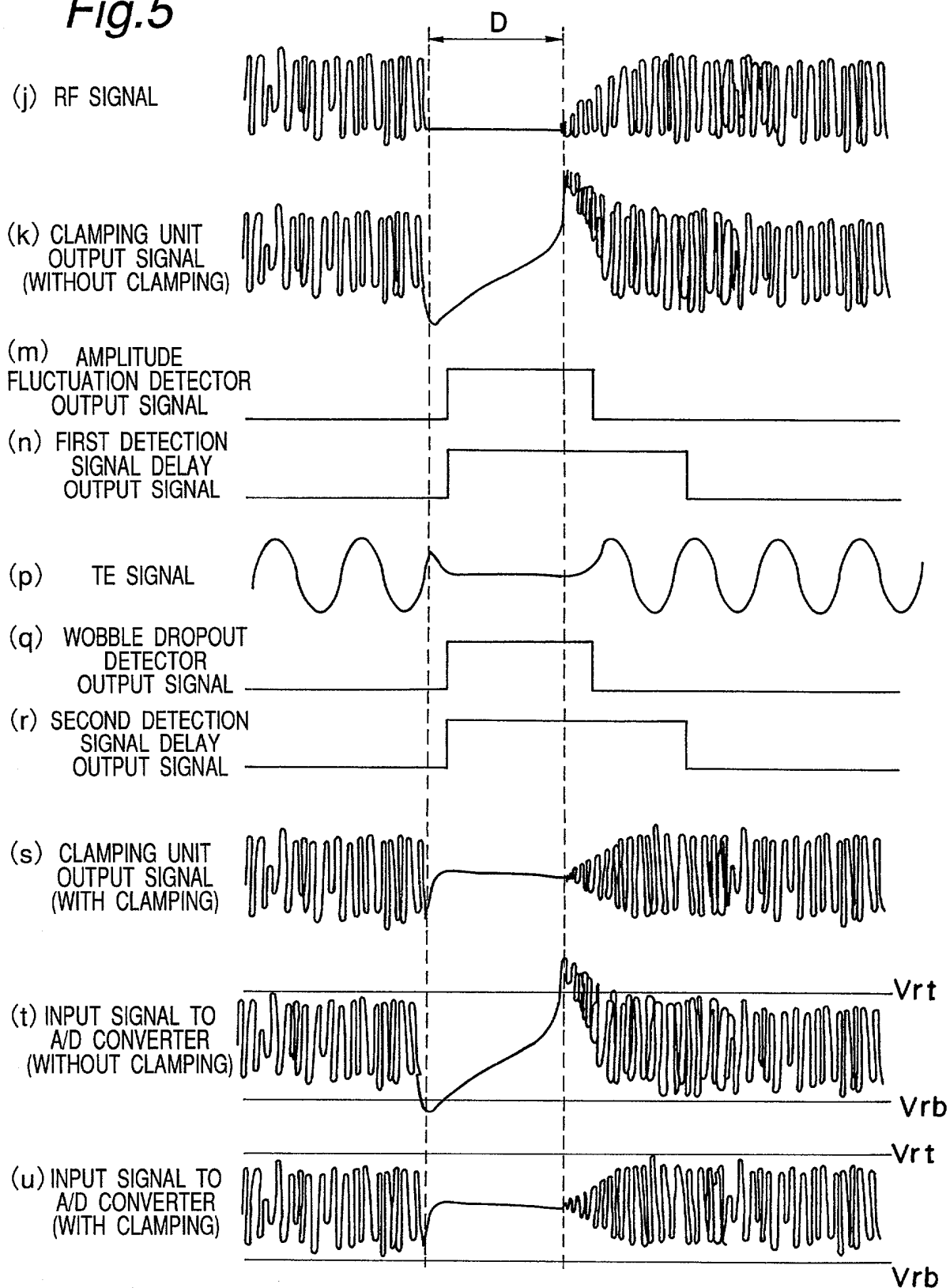


Fig.6

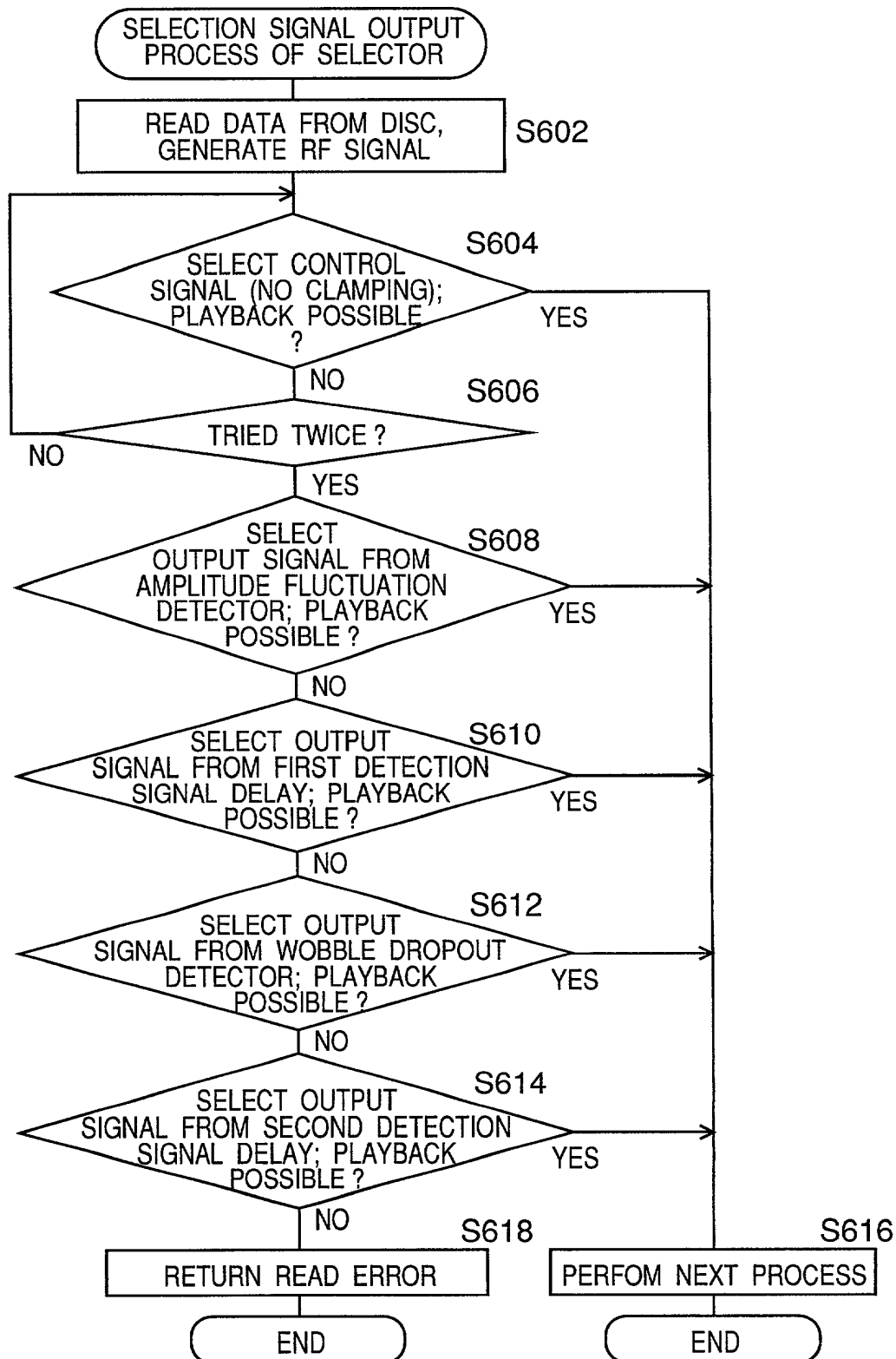


Fig. 7

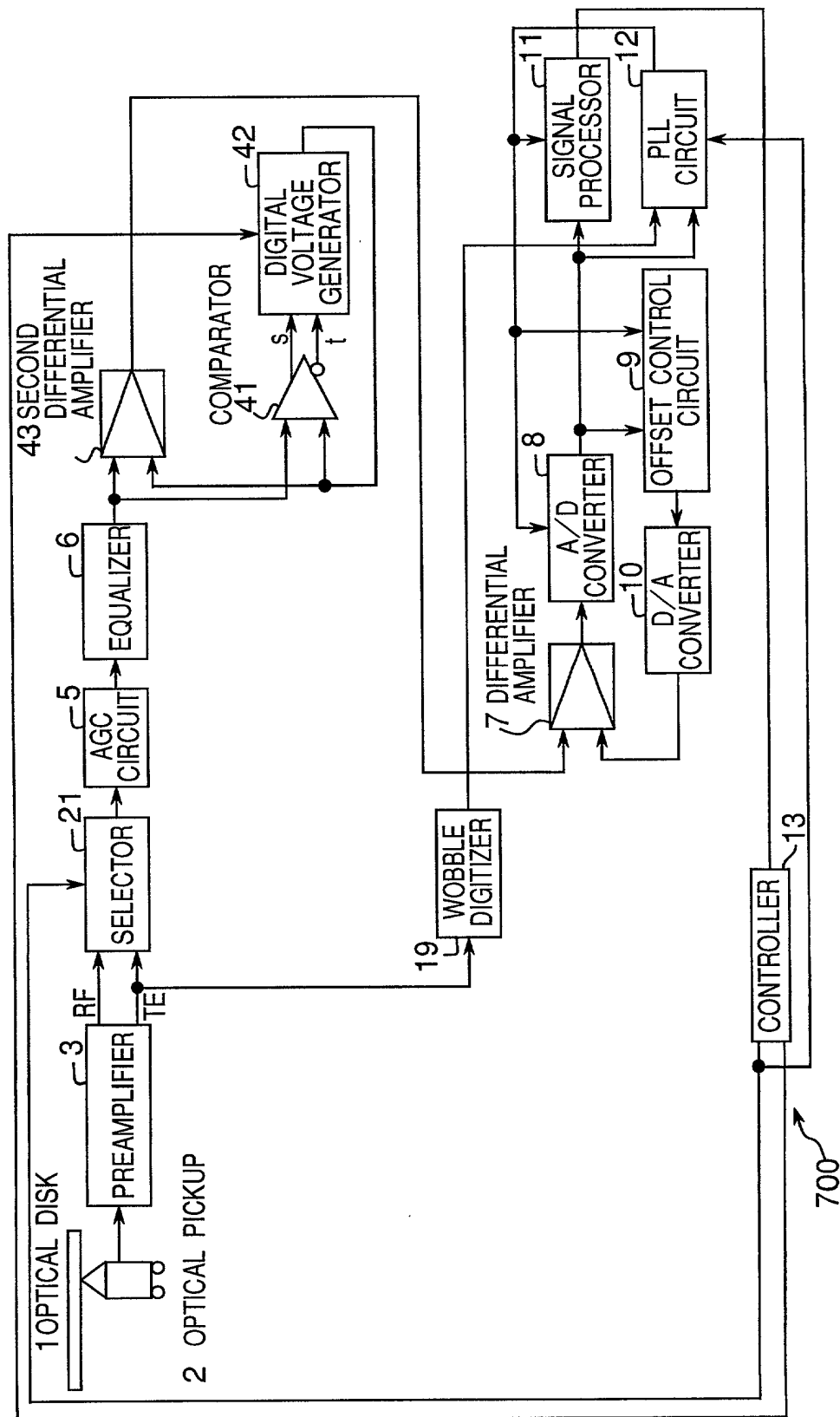


Fig.8

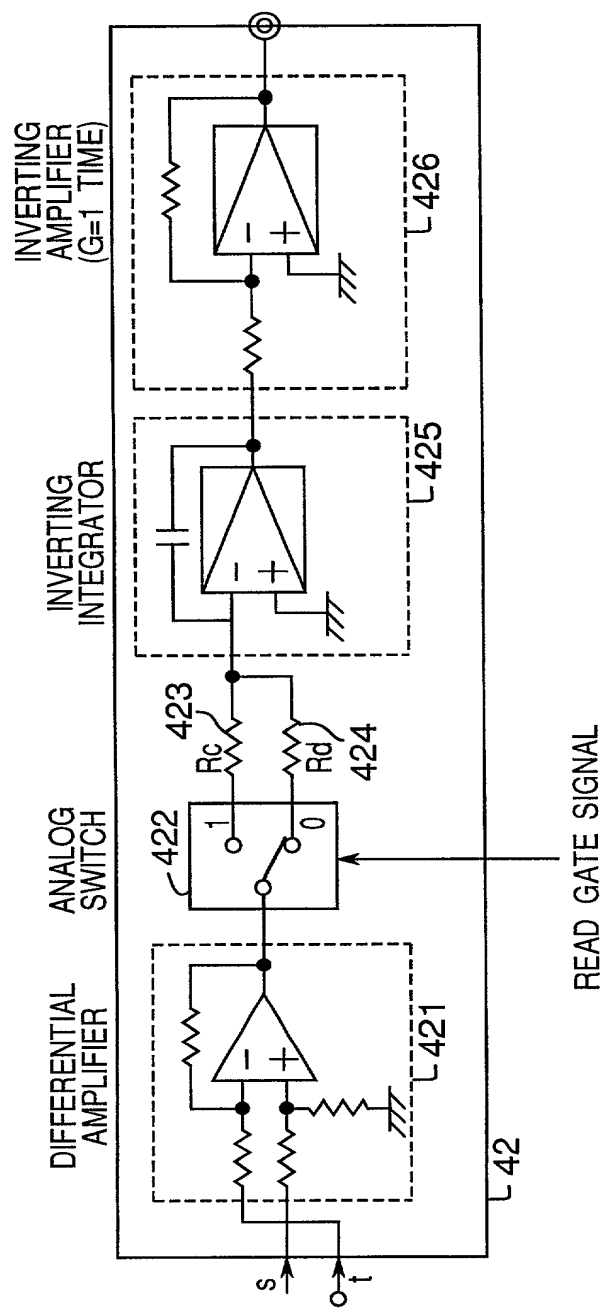




Fig.9

(a) RF SIGNAL



(b) TE SIGNAL



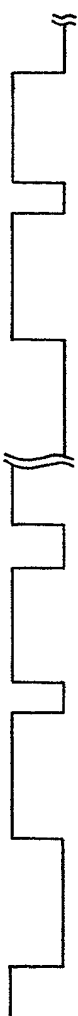
(v) SELECTOR  
OUTPUT SIGNAL



(d) HEADER GATE  
SIGNAL



(e) READ GATE  
SIGNAL



(w) DIGITAL VOLTAGE  
GENERATOR  
OUTPUT SIGNAL



(x) SECOND DIFFERENTIAL  
AMPLIFIER  
OUTPUT SIGNAL



(s) COMPATOR  
OUTPUT SIGNAL  
(V-W)

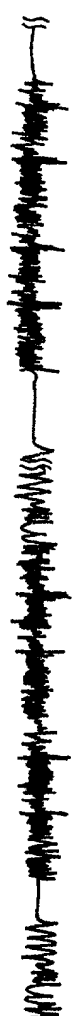


Fig. 10

